IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kevin J. Ryan   Examiner: Unknown

Serial No.:   Unknown   Group Art Unit: Unknown

Filed: Herewith   Docket: 303.306US2

Title: PIPELINED PACKET-ORIENTED MEMORY SYSTEM HAVING A
              UNIDIRECTIONAL COMMAND AND ADDRESS BUS AND A
              BIDIRECTIONAL DATA BUS

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

        Before taking up the above-identified application for examination, please enter the
following amendments.

IN THE SPECIFICATION

        In the first line after the title, please insert the following -- This application is a Divisional
of U.S. Application No. 08/886,753, filed July 1, 1997. --

IN THE CLAIMS

        Please cancel claims 1-4 and 9-28 without prejudice, amend claims 5, 6, and 29-31, and
add new claims 32-56 as follows:

5.   (Amended) A memory system comprising:

        a memory controller;

        a unidirectional command and address bus coupled to the memory controller, the memory
controller communicating commands and addresses to the command and address bus;

        a bidirectional data bus coupled to the memory controller, the memory controller
communicating data information to the bidirectional data bus for a write operation and receiving
the data information from the bidirectional data bus during a read operation; and

        a plurality N of pipelined memory subsystems, wherein each memory subsystem includes:

                a)   a plurality M of memory devices wherein each memory device contains a
        data in and a data out buffer, a column decoder and a row decoder;